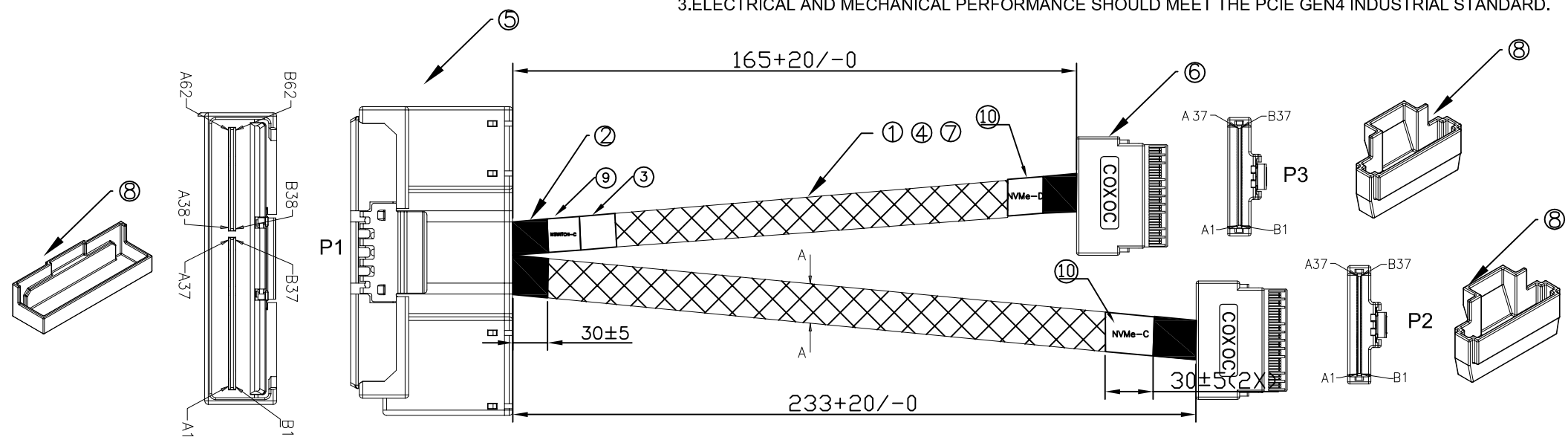


NOTE:  
 1.THE CABLE ASSEMBLY SHOULD BE RoSH2.0 COMPLIANT.  
 2.THE CABLE ASSEMBLY SHALL MEET BELOW IMPEDANCE REQUIREMENT:  
 TERMINATION AREA:85±15% OHMS,CABLE ABSOLUTE:85±10% OHMS  
 3.ELECTRICAL AND MECHANICAL PERFORMANCE SHOULD MEET THE PCIE GEN4 INDUSTRIAL STANDARD.



PART NO

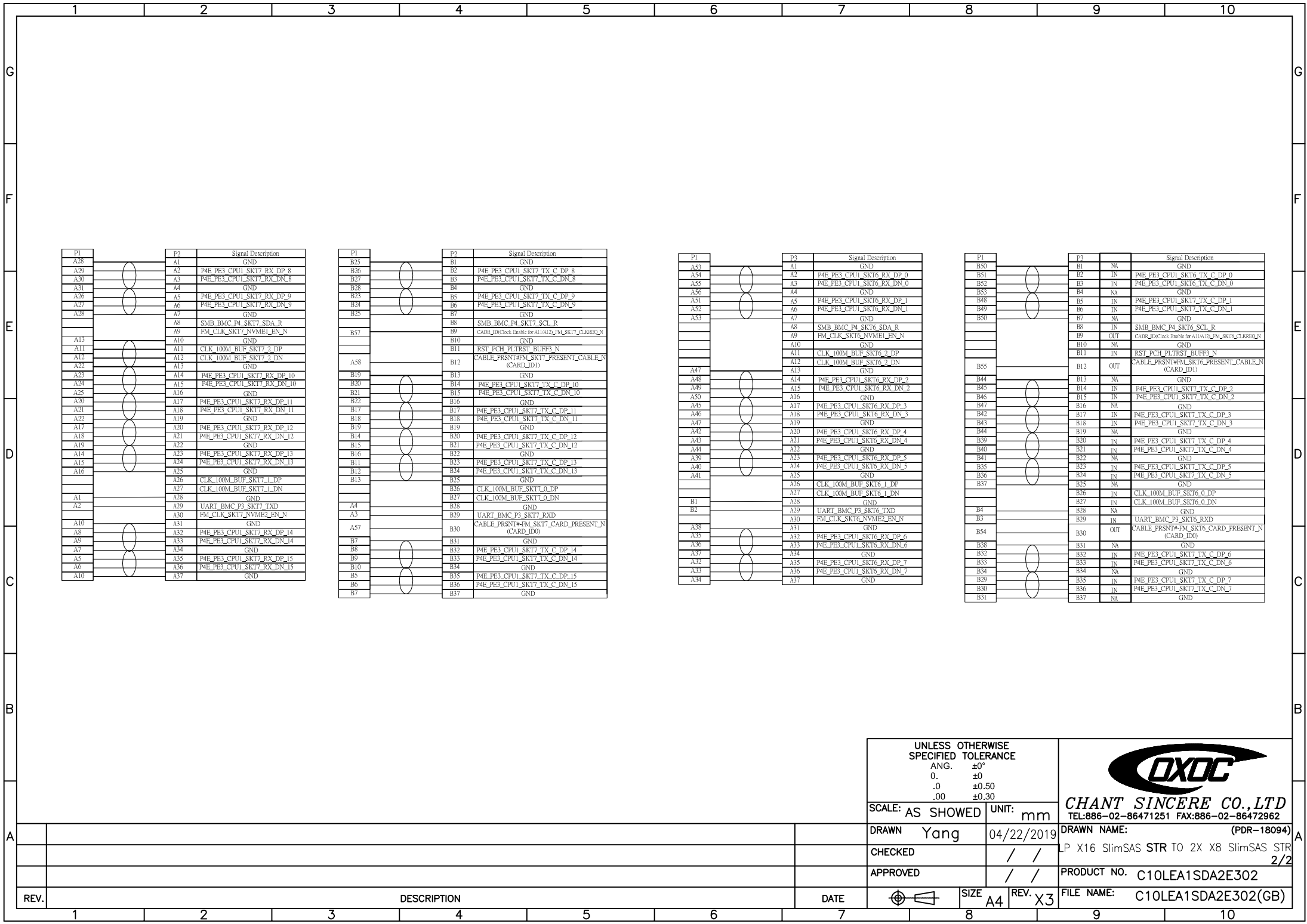
C10 L E A 1 S D A 2 E 3 02  
 1 2 3 4 5 6 7 8 9 10 11 12

- 1.SERIES NO.  
C10=SATA TYPE
- 2.CONNECTOR OPTION  
L=Slimline SAS Male
- 3.Conn.Pin  
E=16X
- 4.Conn.direction  
A=STR
- 5.Conn.number  
1=1PCS
- 6.CONNECTOR OPTION  
S=Slimline SAS Male
- 7.Conn.Pin  
D=8X
- 8.Conn.direction  
A=STR
- 9.Conn.number  
2PCS
- 10.GREEN OPTION:  
E=RoHS
- 11.CABLE COLOR  
3=BLACK
- 12.TYPE

10	LABEL:14*74MM, WHITE	2
9	LABEL:70*26MM, WHITE	1
8	protective cover	3
7	WIRE,30AWG,UL3302,7/0.10,OD0.55,FR-XLPE,VW-1,NO MARKING,HF	A/R
6	SLIMSAS 8i CONNECTOR,STR,Au30u"	2
5	LOW PROFILE 16X SlimSAS Vertical type PLUG,Au30u"	1
4	EXPANDO TUB:7.0mm,BLACK,SINGLE(OD7~25mm)	A/R
3	Label:Synthetic paper,70*40mm, WHITE	1
2	ACETOUS Tape:30mm WIDE,30M/REEL Black	A/R
1	SAS CABLE:UL20744(#30SCC/1P+2D,VW-1.85 Ohm 24G,YEL	A/R
ITEM	DESCRIPTION	QTY

UNLESS OTHERWISE SPECIFIED TOLERANCE		 <b>CHANT SINCERE CO., LTD</b> TEL:886-02-86471251 FAX:886-02-86472962
ANG.	±0°	
0.	±0	
.0	±0.50	
.00	±0.30	
SCALE: AS SHOWN	UNIT: mm	
DRAWN Yang	04/22/2019	DRAWN NAME: (PDR-18094)
CHECKED	/ /	LP X16 SlimSAS STR TO 2X X8 SlimSAS STR 1/2
APPROVED	/ /	PRODUCT NO. C10LEA1SDA2E302
		FILE NAME: C10LEA1SDA2E302(GB)

REV.	DESCRIPTION	DATE	SIZE	REV.
1			A4	X3



P1	P2	Signal Description
A28	A1	GND
A29	A2	P4E_PE3_CPU1_SK17_RX_DP_8
A30	A3	P4E_PE3_CPU1_SK17_RX_DN_8
A31	A4	GND
A36	A5	P4E_PE3_CPU1_SK17_RX_DP_9
A27	A6	P4E_PE3_CPU1_SK17_RX_DN_9
A28	A7	GND
	A8	SMB_BMC_P4_SK17_SDA_R
	A9	FM_CLK_SK17_NVMIE1_EN_N
A13	A10	GND
A11	A11	CLK_100M_BUF_SK17_2_DP
A12	A12	CLK_100M_BUF_SK17_2_DN
A22	A13	GND
A23	A14	P4E_PE3_CPU1_SK17_RX_DP_10
A24	A15	P4E_PE3_CPU1_SK17_RX_DN_10
A25	A16	GND
A20	A17	P4E_PE3_CPU1_SK17_RX_DP_11
A31	A18	P4E_PE3_CPU1_SK17_RX_DN_11
A22	A19	GND
A17	A20	P4E_PE3_CPU1_SK17_RX_DP_12
A18	A21	P4E_PE3_CPU1_SK17_RX_DN_12
A19	A22	GND
A14	A23	P4E_PE3_CPU1_SK17_RX_DP_13
A15	A24	P4E_PE3_CPU1_SK17_RX_DN_13
A16	A25	GND
	A26	CLK_100M_BUF_SK17_1_DP
	A27	CLK_100M_BUF_SK17_1_DN
A1	A28	GND
A2	A29	UART_BMC_P3_SK17_TXD
	A30	FM_CLK_SK17_NVMIE2_EN_N
A10	A31	GND
A8	A32	P4E_PE3_CPU1_SK17_RX_DP_14
A9	A33	P4E_PE3_CPU1_SK17_RX_DN_14
A7	A34	GND
A5	A35	P4E_PE3_CPU1_SK17_RX_DP_15
A6	A36	P4E_PE3_CPU1_SK17_RX_DN_15
A10	A37	GND

P1	P2	Signal Description
B25	B1	GND
B26	B2	P4E_PE3_CPU1_SK17_TX_C_DP_8
B27	B3	P4E_PE3_CPU1_SK17_TX_C_DN_8
B28	B4	GND
B23	B5	P4E_PE3_CPU1_SK17_TX_C_DP_9
B24	B6	P4E_PE3_CPU1_SK17_TX_C_DN_9
B25	B7	GND
	B8	SMB_BMC_P4_SK17_SCL_R
B57	B9	CARD_IDClock Enable for A11A12_P4_SK17_CLKREQ0_N
	B10	GND
	B11	RST_PCH_PL1TRST_BUFF3_N
A58	B12	CABLE_PRSNT#M_SK17_PRESENT_CABLE_N (CARD_ID1)
B19	B13	GND
B20	B14	P4E_PE3_CPU1_SK17_TX_C_DP_10
B21	B15	P4E_PE3_CPU1_SK17_TX_C_DN_10
B22	B16	GND
B17	B17	P4E_PE3_CPU1_SK17_TX_C_DP_11
B18	B18	P4E_PE3_CPU1_SK17_TX_C_DN_11
B19	B19	GND
B14	B20	P4E_PE3_CPU1_SK17_TX_C_DP_12
B15	B21	P4E_PE3_CPU1_SK17_TX_C_DN_12
B16	B22	GND
B11	B23	P4E_PE3_CPU1_SK17_TX_C_DP_13
B12	B24	P4E_PE3_CPU1_SK17_TX_C_DN_13
B13	B25	GND
	B26	CLK_100M_BUF_SK17_0_DP
	B27	CLK_100M_BUF_SK17_0_DN
A4	B28	GND
A3	B29	UART_BMC_P3_SK17_RXD
	B30	CABLE_PRSNT#M_SK17_CARD_PRESENT_N (CARD_ID0)
A57	B31	GND
B7	B32	P4E_PE3_CPU1_SK17_TX_C_DP_14
B8	B33	P4E_PE3_CPU1_SK17_TX_C_DN_14
B9	B34	GND
B10	B35	P4E_PE3_CPU1_SK17_TX_C_DP_15
B5	B36	P4E_PE3_CPU1_SK17_TX_C_DN_15
B6	B37	GND

P1	P3	Signal Description
A53	A1	GND
A54	A2	P4E_PE3_CPU1_SK16_RX_DP_0
A55	A3	P4E_PE3_CPU1_SK16_RX_DN_0
A56	A4	GND
A51	A5	P4E_PE3_CPU1_SK16_RX_DP_1
A52	A6	P4E_PE3_CPU1_SK16_RX_DN_1
A53	A7	GND
	A8	SMB_BMC_P4_SK16_SDA_R
	A9	FM_CLK_SK16_NVMIE1_EN_N
	A10	GND
	A11	CLK_100M_BUF_SK16_2_DP
	A12	CLK_100M_BUF_SK16_2_DN
	A13	GND
A47	A14	P4E_PE3_CPU1_SK16_RX_DP_2
A48	A15	P4E_PE3_CPU1_SK16_RX_DN_2
A49	A16	GND
A50	A17	P4E_PE3_CPU1_SK16_RX_DP_3
A46	A18	P4E_PE3_CPU1_SK16_RX_DN_3
A47	A19	GND
A42	A20	P4E_PE3_CPU1_SK16_RX_DP_4
A43	A21	P4E_PE3_CPU1_SK16_RX_DN_4
A44	A22	GND
A39	A23	P4E_PE3_CPU1_SK16_RX_DP_5
A40	A24	P4E_PE3_CPU1_SK16_RX_DN_5
A41	A25	GND
	A26	CLK_100M_BUF_SK16_1_DP
	A27	CLK_100M_BUF_SK16_1_DN
B1	A28	GND
B2	A29	UART_BMC_P3_SK16_TXD
	A30	FM_CLK_SK16_NVMIE2_EN_N
A38	A31	GND
A35	A32	P4E_PE3_CPU1_SK16_RX_DP_6
A36	A33	P4E_PE3_CPU1_SK16_RX_DN_6
A37	A34	GND
A32	A35	P4E_PE3_CPU1_SK16_RX_DP_7
A33	A36	P4E_PE3_CPU1_SK16_RX_DN_7
A34	A37	GND

P1	P3	Signal Description
B50	B1	NA
B51	B2	IN
B52	B3	IN
B53	B4	NA
B48	B5	IN
B49	B6	IN
B50	B7	NA
	B8	IN
	B9	OUT
	B10	NA
	B11	IN
	B12	OUT
B55	B13	NA
B54	B14	IN
B46	B15	IN
B47	B16	NA
B42	B17	IN
B43	B18	IN
B44	B19	NA
B39	B20	IN
B40	B21	IN
B41	B22	NA
B35	B23	IN
B36	B24	IN
B37	B25	NA
	B26	IN
	B27	IN
B4	B28	NA
B3	B29	IN
B54	B30	OUT
B38	B31	NA
B32	B32	IN
B33	B33	IN
B34	B34	NA
B29	B35	IN
B30	B36	IN
B31	B37	NA

UNLESS OTHERWISE SPECIFIED TOLERANCE		
ANG. ±0°		
0. ±0		<b>CHANT SINCERE CO., LTD</b> TEL:886-02-86471251 FAX:886-02-86472962
.0 ±0.50		
.00 ±0.30		
SCALE: AS SHOWN	UNIT: mm	
DRAWN Yang	04/22/2019	DRAWN NAME: (PDR-18094)
CHECKED / /		LP X16 SlimSAS STR TO 2X X8 SlimSAS STR 2/2
APPROVED / /		PRODUCT NO. C10LEA1SDA2E302
SIZE A4 REV. X3		FILE NAME: C10LEA1SDA2E302(GB)

REV.	DESCRIPTION	DATE
1		
2		
3		
4		
5		
6		
7		