



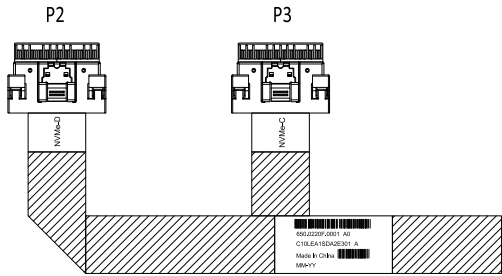
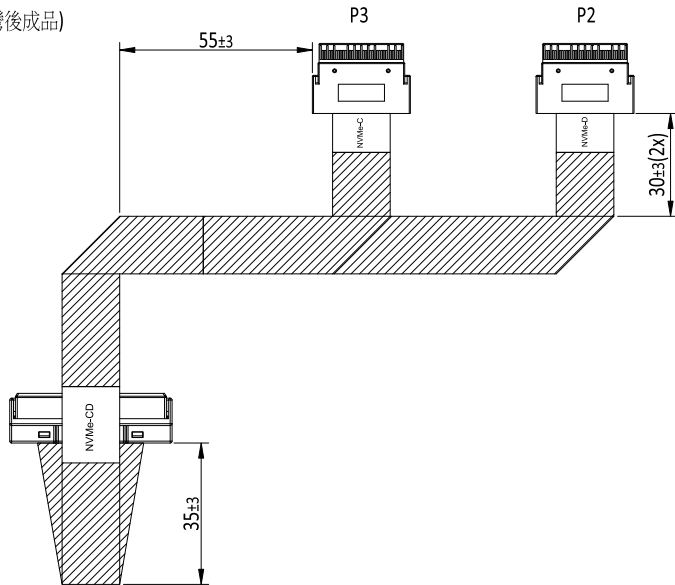
1	15101040010	C10LEA1SDA2E301	1				
ITEM	NEW P/N	OLD P/N	NOTE				
UNLESS OTHERWISE SPECIFIED TOLERANCE ANG. ±° 0. ±0.50 .0 ±0.25 .00 ±0.15		 SIP  Cmk Cpk	 CHANT SINCERE CO.,LTD				
SCALE:	AS SHOWN	UNIT: mm					
DRAWN:	HOLLY_TU	01 Apr 2024				TITLE: PDR-20048 SLIMSAS LP 16X STR TO SLIMSAS 8X STRx2 CABLE	
CHECKED:	PEARL_HUNG	02 Apr 2024					
APPROVED: PEARL_HUNG		02 Apr 2024	PART NO.	15101040010	C		
	SIZE A4 SHEET 1/3	REV. B	CODE: p09	CUSTOMER:			

	B	ECN-2403108(JOLIN) ADD NEW P/N
	A	ECN-2204054(JERRY)X13 CHANGE TO A
ZONE	REVISION	DESCRIPTION

RoHS
compliant



預折彎後
(折彎後成品)



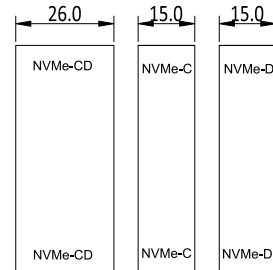
主標籤視圖



LABEL DETAIL

READING: 650.0220F.0001 A0
CODE 128
WISTRON PN
COXOC PN & REV.
READING: MADE IN CHINA
CODE 128
DATE CODE

TEXT FONT: ARIAL
TEXT HEIGHT: 3mm MIN.
BARCODE HEIGHT: 3.175mm
字體: ARIAL
字高: 3mm MIN.
條形碼高度: 3.175mm



					UNLESS OTHERWISE SPECIFIED TOLERANCE		SIP ST CmkCpk		 CHANT SINCERE CO.,LTD					
					SCALE: AS SHOWN		UNIT: mm							
					DRAWN: HOLLY_TU		01 Apr 2024		TITLE: PDR-20048 SLIMSAS LP 16X STR TO SLIMSAS 8X STRx2 CABLE					
					CHECKED: PEARL_HUNG		02 Apr 2024							
					APPROVED: PEARL_HUNG		02 Apr 2024		PART NO. 15101040010		C			
ZONE		REVISION		DESCRIPTION			DATE		SIZE A4 SHEET 2/3		REV. B	CODE: P09	CUSTOMER:	
1	2	3	4	5	6	7	8	9	10					

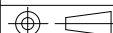
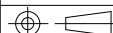
P1	P2
A31	A1 GND
A33	A2 P4E_PEB_CPU2_SKT9_RX_DP_8
A32	A3 P4E_PEB_CPU2_SKT9_RX_DN_8
A34	A4 GND
A30	A5 P4E_PEB_CPU2_SKT9_RX_DP_9
A29	A6 P4E_PEB_CPU2_SKT9_RX_DN_9
A31	A7 GND
	A8 SMB_BMC_P4_SKT9_SDA_R
	A9 FM_CLK_SKT9_NVMEM_EN_N
	A10 GND
B1	A11 CLK_100M_BUF_SKT9_0_DP
B3	A12 CLK_100M_BUF_SKT9_0_DN
B2	
B4	A13 GND
A27	A14 P4E_PEB_CPU2_SKT9_RX_DP_10
A26	A15 P4E_PEB_CPU2_SKT9_RX_DN_10
A28	A16 GND
A24	A17 P4E_PEB_CPU2_SKT9_RX_DP_11
A23	A18 P4E_PEB_CPU2_SKT9_RX_DN_11
A25	A19 GND
A21	A20 P4E_PEB_CPU2_SKT9_RX_DP_12
A20	A21 P4E_PEB_CPU2_SKT9_RX_DN_12
A22	A22 GND
A18	A23 P4E_PEB_CPU2_SKT9_RX_DP_13
A17	A24 P4E_PEB_CPU2_SKT9_RX_DN_13
A19	A25 GND
	A26 CLK_100M_BUF_SKT9_1_DP
	A27 CLK_100M_BUF_SKT9_1_DN
	A28 GND
	A29 UART_BMC_P3_SKT9_RXD
	A30 FM_CLK_SKT9_NVMEM_EN_N
A16	A31 GND
A15	A32 P4E_PEB_CPU2_SKT9_RX_DP_14
A14	A33 P4E_PEB_CPU2_SKT9_RX_DN_14
A13	A34 GND
A12	A35 P4E_PEB_CPU2_SKT9_RX_DP_15
A11	A36 P4E_PEB_CPU2_SKT9_RX_DN_15
A10	A37 GND

P1	P2
B28	B1 GND
B30	B2 P4E_PEB_CPU2_SKT9_TX_C_DP_8
B29	B3 P4E_PEB_CPU2_SKT9_TX_C_DN_8
B31	B4 GND
B27	B5 P4E_PEB_CPU2_SKT9_TX_C_DP_9
B26	B6 P4E_PEB_CPU2_SKT9_TX_C_DN_9
B28	B7 GND
	B8 SMB_BMC_P4_SKT9_SCL_R
B60	B9 CARD_ID (Clock Enable for A11/A12) FM_SKT9_CLKREQ_N
	B10 GND
	B11 RST_PCH_PLTRST_BUFF3_N
A9	B12 CABLE_PRSTNTRFM_SKT9_PRESENT_CABLE_N (CARD_ID1)
B22	B13 GND
B24	B14 P4E_PEB_CPU2_SKT9_TX_C_DP_10
B23	B15 P4E_PEB_CPU2_SKT9_TX_C_DN_10
B25	B16 GND
B21	B17 P4E_PEB_CPU2_SKT9_TX_C_DP_11
B20	B18 P4E_PEB_CPU2_SKT9_TX_C_DN_11
B22	B19 GND
B18	B20 P4E_PEB_CPU2_SKT9_TX_C_DP_12
B17	B21 P4E_PEB_CPU2_SKT9_TX_C_DN_12
B19	B22 GND
B15	B23 P4E_PEB_CPU2_SKT9_TX_C_DP_13
B14	B24 P4E_PEB_CPU2_SKT9_TX_C_DN_13
B16	B25 GND
	B26 CLK_100M_BUF_SKT9_2_DP
	B27 CLK_100M_BUF_SKT9_2_DN
	B28 GND
	B29 UART_BMC_P3_SKT9_TXD
A8	B30 CARD_PRSTNTRFM_SKT9_CARD_PRESENT_N (CARD_ID0)
B10	B31 GND
B12	B32 P4E_PEB_CPU2_SKT9_TX_C_DP_14
B11	B33 P4E_PEB_CPU2_SKT9_TX_C_DN_14
B13	B34 GND
B9	B35 P4E_PEB_CPU2_SKT9_TX_C_DP_15
B8	B36 P4E_PEB_CPU2_SKT9_TX_C_DN_15
B10	B37 GND

P1	P3
A56	A1 GND
A58	A2 P4E_PEB_CPU2_SKT8_RX_DP_0
A57	A3 P4E_PEB_CPU2_SKT8_RX_DN_0
A59	A4 GND
A55	A5 P4E_PEB_CPU2_SKT8_RX_DP_1
A54	A6 P4E_PEB_CPU2_SKT8_RX_DN_1
A56	A7 GND
A6	A8 SMB_BMC_P4_SKT8_SDA_R
	A9 FM_CLK_SKT8_NVMEM_EN_N
A1	A10 GND
A3	A11 CLK_100M_BUF_SKT8_0_DP
A2	A12 CLK_100M_BUF_SKT8_0_DN
A4/A53	A13 GND
A52	A14 P4E_PEB_CPU2_SKT8_RX_DP_2
A51	A15 P4E_PEB_CPU2_SKT8_RX_DN_2
A50	A16 GND
A49	A17 P4E_PEB_CPU2_SKT8_RX_DP_3
A48	A18 P4E_PEB_CPU2_SKT8_RX_DN_3
A50	A19 GND
A46	A20 P4E_PEB_CPU2_SKT8_RX_DP_4
A45	A21 P4E_PEB_CPU2_SKT8_RX_DN_4
A47	A22 GND
A43	A23 P4E_PEB_CPU2_SKT8_RX_DP_5
A42	A24 P4E_PEB_CPU2_SKT8_RX_DN_5
A44	A25 GND
	A26 CLK_100M_BUF_SKT8_1_DP
	A27 CLK_100M_BUF_SKT8_1_DN
	A28 GND
B5	A29 UART_BMC_P3_SKT8_TXD
	A30 FM_CLK_SKT8_NVMEM_EN_N
A41	A31 GND
A40	A32 P4E_PEB_CPU2_SKT8_RX_DP_6
A39	A33 P4E_PEB_CPU2_SKT8_RX_DN_6
A38	A34 GND
A36	A35 P4E_PEB_CPU2_SKT8_RX_DP_7
A35	A36 P4E_PEB_CPU2_SKT8_RX_DN_7
A37	A37 GND

P1	P3
B56	B1 GND
B55	B2 P4E_PEB_CPU2_SKT8_TX_C_DP_0
B54	B3 P4E_PEB_CPU2_SKT8_TX_C_DN_0
B53	B4 GND
B52	B5 P4E_PEB_CPU2_SKT8_TX_C_DP_1
B51	B6 P4E_PEB_CPU2_SKT8_TX_C_DN_1
B50	B7 GND
A5	B8 SMB_BMC_P4_SKT8_SCL_R
A60	B9 CARD_ID (Clock Enable for A11/A12) FM_SKT8_CLKREQ_N
A59	B10 GND
	B11 RST_PCH_PLTRST_BUFF3_N
B58	B12 CABLE_PRSTNTRFM_SKT8_PRESENT_CABLE_N (CARD_ID1)
B50	B13 GND
B49	B14 P4E_PEB_CPU2_SKT8_TX_C_DP_2
B48	B15 P4E_PEB_CPU2_SKT8_TX_C_DN_2
B47	B16 GND
B46	B17 P4E_PEB_CPU2_SKT8_TX_C_DP_3
B45	B18 P4E_PEB_CPU2_SKT8_TX_C_DN_3
B44	B19 GND
B43	B20 P4E_PEB_CPU2_SKT8_TX_C_DP_4
B42	B21 P4E_PEB_CPU2_SKT8_TX_C_DN_4
B41	B22 GND
B40	B23 P4E_PEB_CPU2_SKT8_TX_C_DP_5
B39	B24 P4E_PEB_CPU2_SKT8_TX_C_DN_5
B38	B25 GND
	B26 CLK_100M_BUF_SKT8_2_DP
	B27 CLK_100M_BUF_SKT8_2_DN
A7	B28 GND
B6	B29 UART_BMC_P3_SKT8_RXD
B57	B30 CARD_PRSTNTRFM_SKT8_CARD_PRESENT_N (CARD_ID0)
B34	B31 GND
B36	B32 P4E_PEB_CPU2_SKT8_TX_C_DP_6
B35	B33 P4E_PEB_CPU2_SKT8_TX_C_DN_6
B37	B34 GND
B33	B35 P4E_PEB_CPU2_SKT8_TX_C_DP_7
B32	B36 P4E_PEB_CPU2_SKT8_TX_C_DN_7
B34	B37 GND

WIRE DIAGRAM

UNLESS OTHERWISE SPECIFIED TOLERANCE			 SIP		 CHANT SINCERE CO.,LTD	
ANG. $\pm 2^{\circ}$ 0. ± 0.50 0. ± 0.25 .00 ± 0.15			 Cmk Cpk			
SCALE: AS SHOWN			UNIT: mm			
DRAWN: HOLLY_TU			01 Apr 2024		TITLE: PDR-20048 SLIMSAS LP 16X STR TO SLIMSAS 8X STRx2 CABLE	
CHECKED: PEARL_HUNG			02 Apr 2024			
APPROVED: PEARL_HUNG			02 Apr 2024		PART NO. 15101040010	
 SIZE A4			SHEET 3/3		REV. B	
			7		8	
					9	
					10	

		B	ECN-2403108(JOLIN) ADD NEW P/N	29MAR2024	
		A	ECN-2204054(JERRY)X13 CHANGE TO A	05MAY2022	
ZONE	REVISION	DESCRIPTION		DATE	
1	2	3	4	5	6